

AMENDMENTS TO THE CLAIMS

1-7. (Cancelled)

8. (Currently Amended) The system of claim [[44]] 47, further comprising:

a plurality of detectors, each one of said plurality of detectors configured to detect one of a plurality of packetized digital communication signals, each one of said plurality of packetized digital communication signals being uniquely associated with one of a plurality of transmitter units residing in said communication device, and each one of said plurality of detectors configured to generate a control signal in response to the detection of said packetized digital communication signal; and

a plurality of transmitter power managers, each one of said plurality of transmitter power managers coupled to the corresponding one of said plurality of detectors and configured to receive said control signal from said detector, and each one of said plurality of transmitter power managers coupled to at least one element residing in the corresponding one of said plurality of transmitter units,

such that when one of said detectors detects said corresponding packetized digital communication signal and generates said control signal, said corresponding transmitter power manager provides power to said at least one element in response to the detection of said packetized digital communication signal.

9. (Original) The system of claim 8, further comprising a plurality of switchable devices, at least one switchable device residing in each one of said plurality of transmitter power managers, each one of said plurality of switchable devices responsive to said control signal

associated with said transmitter power manager in which said at least one switchable device resides, such that said at least one element is coupled to a power supply in response to the detection of said associated packetized digital communication signal.

10. (Original) The system of claim 9, wherein at least one of said plurality of switchable devices is a transistor, said transistor coupled to one of said plurality of detectors such that said control signal actuates said transistor into a conducting state so that said at least one element is coupled to said power supply.

11-44. (Cancelled)

45. (Cancelled)

46. (Currently Amended) The apparatus system of claim [[44]] 47, wherein said detector is further configured to generate a control signal in response to the detection, and said transmitter power manager is further configured to provide power to the first and second elements in response to the control signal.

47. (Currently Amended) The apparatus of claim 44, further A system for controlling power in a transmitter unit in a communication device for use on a telephony subscriber loop, comprising:

a detector configured to detect the presence of an incoming packetized digital signal for transmission on the subscriber loop;

a transmitter power manager coupled to said detector, said transmitter power manager configured to provide power, in response to the detection, to a first element residing in said

transmitter unit, and further configured to provide power, after a delay, to a second element residing in said transmitter unit;

transmit signal generating circuitry configured to receive and process said packetized digital signal; and

a transmitter coupled to said transmit signal generating circuitry and configured to amplify and transmit said processed signal onto said telephony system subscriber loop,

wherein the first at least one element resides in the transmit signal generating circuitry, and the second at least one element resides in the transmitter.

48. (Currently Amended) The apparatus system of claim [[46]] 47, wherein said detector is coupled to said transmitter generating circuitry.

49. (Currently Amended) The apparatus system of claim [[46]] 47, wherein said transmitter generating circuitry comprises a digital signal processor (DSP).

50. (Currently Amended) The apparatus system of claim [[44]] 47, wherein said detector is further configured to detect the absence of said packetized digital signal and to remove power, in response to the absence, to at least one of the first and the second element.

51. (Currently Amended) The apparatus system of claim [[45]] 47, further comprising a power supply, wherein said transmitter power manager comprises:

 a first switchable device coupled to said at least one first element and configured to couple said at least one first element to said power supply in response to the detection; and

 a second switchable device coupled to said at least one second element and configured to couple said at least one second element to said power supply in response to the detection.

52. (Currently Amended) The apparatus system of claim [[50]] 51, wherein said first switchable device is a transistor, said transistor coupled to said detector such that a control signal activates said transistor into a conducting state to couple said at least one first element to said power supply, said control signal generated by said detector in response to the detection.

53. (Cancelled)

54. (Currently Amended) The apparatus system of claim [[52]] 55, wherein said detector is further configured to generate a control signal in response to the detection, and said transmitter power manager is further configured to provide power to the first and second elements in response to the control signal.

55. (Currently Amended) The apparatus of claim 52, further A communication system for use on a telephony subscriber loop, comprising:

a detector configured to detect the presence of an incoming packetized digital signal for transmission on the subscriber loop;

a transmitter configured to amplify and transmit said packetized digital signal onto said subscriber loop;

a transmitter power manager coupled to said detector, said transmitter power manager configured to provide power, in response to the detection, to a first element residing in said transmitter, and further configured to provide power, after a delay, to a second element residing in said transmitter; and

transmit signal generating circuitry configured to receive and process said packetized digital signal; and

wherein the first at least one element resides in the transmit signal generating circuitry, and the second at least one element resides in the transmitter.

56. (Currently Amended) The apparatus system of claim [[54]] 55, wherein said detector is coupled to said transmitter generating circuitry.

57. (Currently Amended) The apparatus system of claim [[54]] 55, wherein said transmitter generating circuitry comprises a digital signal processor (DSP).

58. (Currently Amended) The apparatus system of claim [[52]] 55, wherein said detector is further configured to detect the absence of said packetized digital signal and to remove power, in response to the absence, to at least one of the first and the second element.

59. (Currently Amended) The apparatus system of claim [[52]] 55, further comprising a power supply, wherein said transmitter power manager comprises:

a first switchable device coupled to said at least one first element and configured to couple said at least one first element to said power supply in response to the detection; and

a second switchable device coupled to said at least one second element and configured to couple said at least one second element to said power supply in response to the detection.

60. (Currently Amended) The apparatus system of claim [[58]] 59, wherein said first switchable device is a transistor, said transistor coupled to said detector such that a control signal activates said transistor into a conducting state to couple said at least one first element to said power supply, said control signal generated by said detector in response to the detection.

61. (Cancelled)

62. (Currently Amended) The method of claim [[61]] 66, further comprising the steps of:

generating a control signal in response to the presence of said packetized digital signal transmitted; and

providing power to the first and second elements in response to the control signal.

63. (Currently Amended) The method of claim [[61]] 66, further comprising the steps of:

detecting the absence of said packetized digital signal; and

removing power, in response to the absence, to at least one of the first and the second element.

64. (Currently Amended) The method of claim[[61]] 66, wherein the predefined delay is based on the stabilization time of the first element.

65. (Currently Amended) The method of claim [[61]] 66, wherein the predefined delay is based on the stabilization time of the first element as compared to the stabilization time of the second element.

66. (Currently Amended) ~~The method of claim 61, further comprising the step of A method for controlling power in a transmitter unit in a communication device, comprising the steps of:~~

detecting the presence of a packetized digital signal transmitted onto a telephony subscriber loop by said transmitter unit;

providing power, in response to the detection, to a first at least one element residing in said transmitter unit;

providing power, after a predefined delay, to a second at least one element residing in said transmitter unit; and

propagating said packetized digital signal through a first series of components residing in the transmitter unit, said propagating resulting in a propagation delay, wherein the predefined delay is based on the propagation delay.

67. (Previously Presented) An apparatus for controlling power in a transmitter unit in a communication device, comprising:

a transmitter power manager;

transmit signal generating circuitry configured to receive and process a packetized digital signal, said circuitry comprising:

a plurality of constant-power components, said plurality of constant-power components operating to process the signal in a serial manner, each constant-power component having a processing time; and

at least one power-down component having an activation time;

said apparatus further comprising a detector configured to detect the presence of said packetized digital communication signal by monitoring activity of one of the constant-power components having a processing time greater than the power-down component activation time,

said transmitter power manager further configured to supply power to the power-down component responsive to the detection by said detector.

68. (Previously Presented) The apparatus of claim 67, wherein said activation time includes a turn-on time and a stabilization time.

69. (Previously Presented) The apparatus of claim 67, wherein said detector is further configured to select one of said first series components having an associated processing time greater than said power-up time of said second component and to monitor the selected first series component to detect the presence of said packetized digital communication signal.

70. (Previously Presented) A method for controlling power in a transmitter unit in a communication device, comprising the steps of:

processing a packetized digital signal in a serial manner by a first series of components residing in the transmitter unit, each component in said first series having an associated processing time; and

detecting the presence of said packetized digital communication signal by monitoring the processing activity of one of said first series components having an associated processing time greater than a power-up time of a second component residing in said transmitter unit; and supplying power to said second component responsive to the detection.

71. (Previously Presented) The method of claim 70, wherein said power-up time includes a turn-on time and a stabilization time.

72. (Previously Presented) The method of claim 70, further comprising the step of:

selecting one of said first series components having an associated processing time greater than said power-up time of said second component; and

monitoring the selected first series component to detect the presence of said packetized digital communication signal.